

1.Specifications:

Model	MD9260A5-4G
Chipset	AMD Embedded E9260
Shader Processing Units	896 Shaders
Bus Type	MXM 3.1 / up to PCI Express 3.0
Graphics Clock	1053MHz
Single Precision FLOPS	1860GFLOPS
Double Precision FLOPS	115.9GFLOPS
Memory Size	4GB 128bit GDDR5
Memory Clock	1500MHz (6.0Gbps)
Memory Bandwidth	96.0GB/s
Display Features	DP_A: HDMI2.0 DP_B: HDMI2.0 DP_C: HDMI2.0 DP_D: HDMI2.0
Max Resolution	HDMI:4320*2160@60Hz
Number of output Channel	4
Board Power	43W
Board Dimensions	MXM Graphics Module Version 3.0 Type A (72x82mm)
Operation System	Windows 7 32/64 、 Windows 10 64bit 、 Linux 64bit
VIN Range	DC 12~19V, 3.3V & 5V; +/-5%

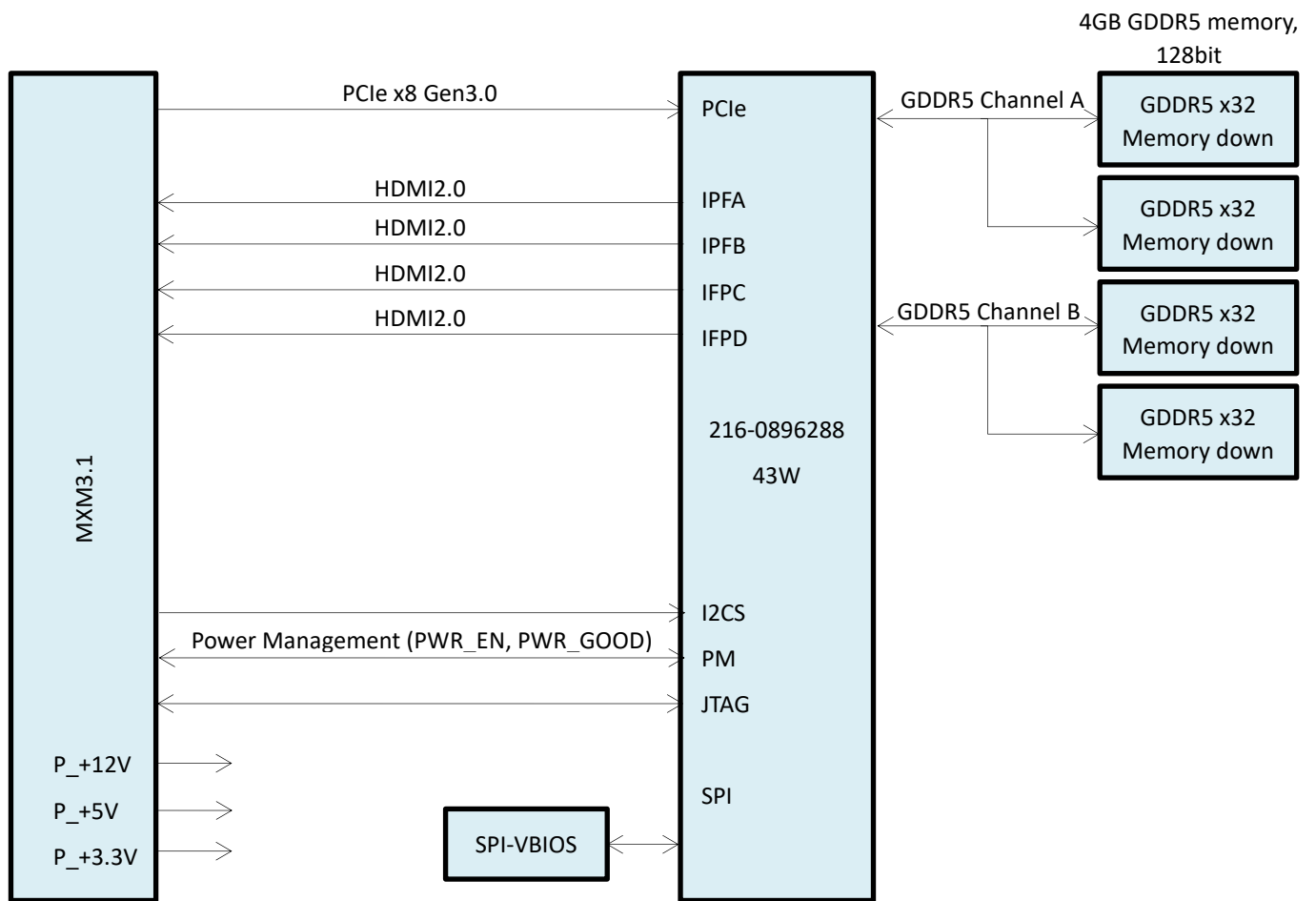
Features	
OpenCL	1.2
OpenGL	4.6
DirectX	12
Supported Rendering Format	
Decode	4K H.264 、 H.265/HEVC
Encode	4K H.264 、 H.265/HEVC

Operating Temperature	0~45°C (Option -20°C)
Storage Temperature	-20~75°C
Operating Humidity	0~95% (non-condensing)
Storage Humidity	10~90%

Surround (Landscape)	Surround (Portrait)
2x1(3840x1080@60Hz)	2x1(2160x1920@60Hz)
1x2(1920x2160@60Hz)	1x2(1080x3840@60Hz)
1x3(1920x3240@60Hz)	1x3(1080x5760@60Hz)
3x1(5760x1080@60Hz)	3x1(3240x1920@60Hz)
4x1(7680x1080@60Hz)	1x4(1080x7680@60Hz)
2x2(3840x2160@60Hz)	2x2(2160x3840@60Hz)

**Total resolution based on every display resolution is 1920*1080@60Hz.*

2.Block Diagram:



3.MXM Board Outlines:

Figure 3.1 shows the board outlines (top side view) for Type A MXM modules. An additional system keep-out of 0.5mm[0.020] per side is allowed on the PCB to accommodate whatever means of production panelization is required. This additional clearance is above and beyond the dimensional limits presented here. The location of these features is not specified or controlled.

Figure 3.1: Board Outlines

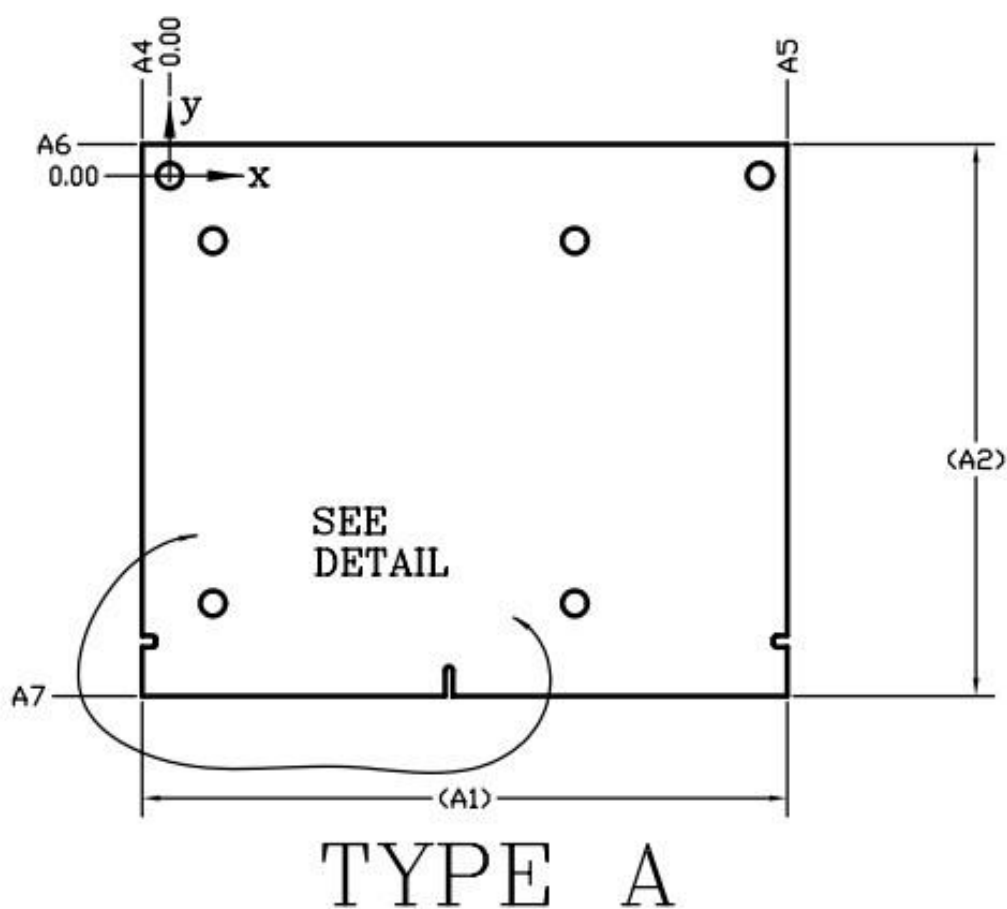


Table 3.1: Board Outline Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
A1		82.00			3.228	
A2		70.00			2.756	
A4	3.37	3.50	3.63	0.133	0.138	0.143
A5	78.37	78.50	78.63	3.085	3.091	3.096
A6	3.87	4.00	4.13	0.152	0.157	0.163
A7	65.87	66.00	66.13	2.593	2.598	2.604

Figure 3.2: Board Slots Detail

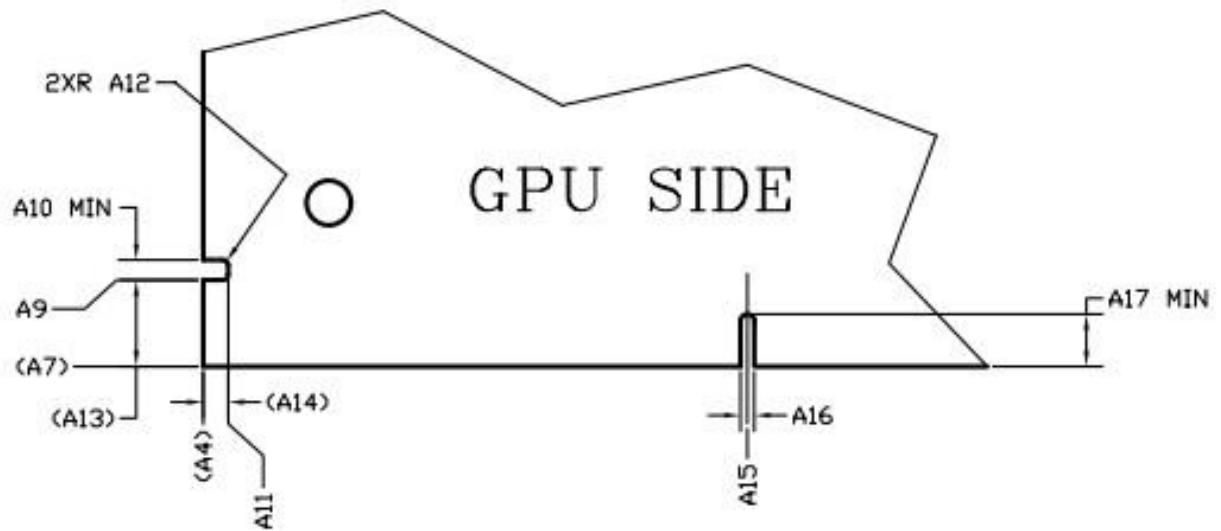


Table 3.2: Board Slots Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
A4		3.50			0.138	
A7		66.00			2.598	
A9	59.67	59.80	59.93	2.349	2.354	2.359
A10	1.45			0.057		
A11	1.57	1.70	1.83	0.062	0.067	0.072
A12	0.32	0.50	0.58	0.013	0.020	0.023
A13		6.20			0.244	
A14		1.80			0.071	
A15	35.37	35.50	35.63	1.393	1.398	1.403
A16	0.95	1.00	1.05	0.037	0.039	0.041
A17	3.75			0.148		

4.MXM PCB Mounting Holes:

All MXM version 3.1 modules have 6 holes. Two are used to secure the board to the system and the other four to fasten the thermal solution to the module.

Figure 4.1: Mounting Holes

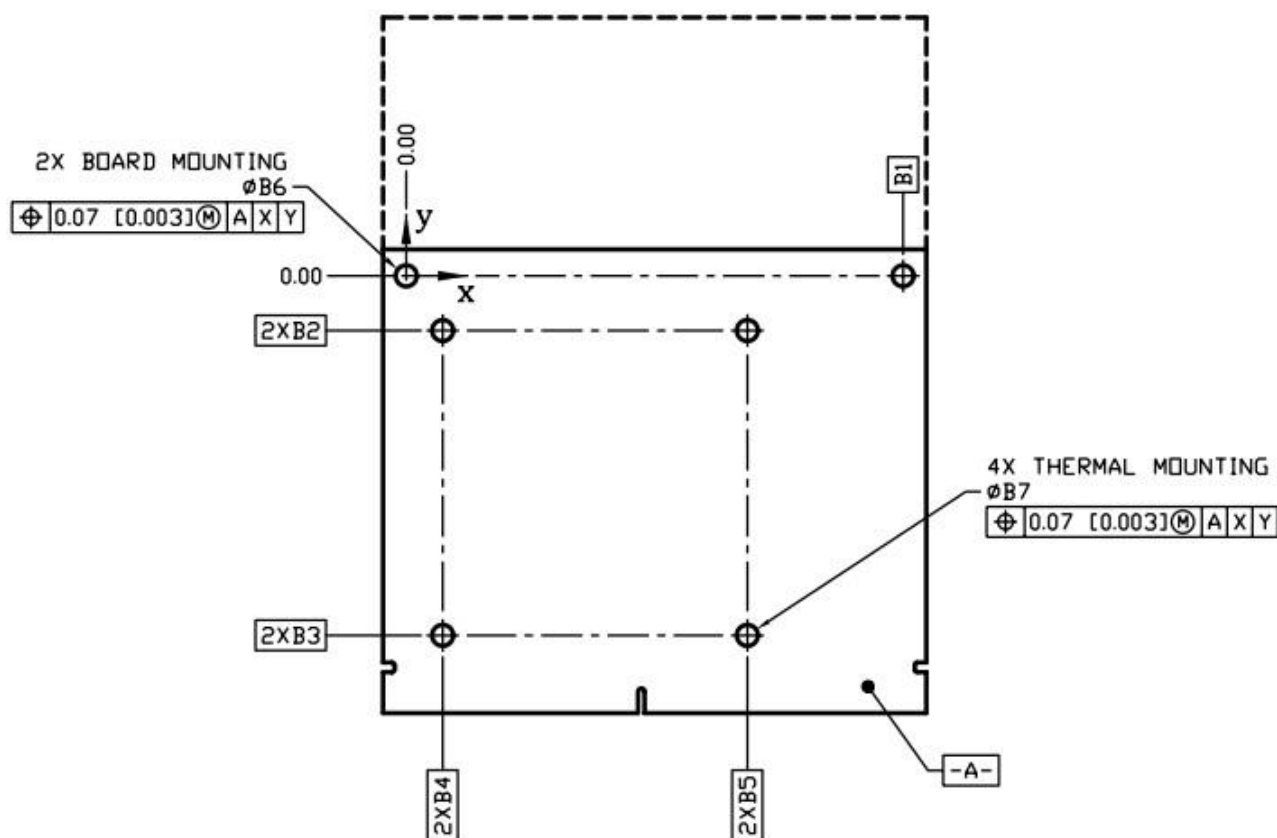


Table 4.1: Mounting Holes Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
B1		75.00			2.953	
B2		8.25			0.325	
B3		54.25			2.136	
B4		5.50			0.217	
B5		51.50			2.028	
B6	3.07	3.20	3.33	0.121	0.126	0.131
B7	3.07	3.20	3.33	0.121	0.126	0.131

5.Connector Pinout:

Table 5.1, Table 5.2 and Table 5.3 list the connector pinout.

Table 5.1: Connector Pinout

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
E1	PWR_SRC	E2	PWR_SRC	57	PEX_RX14	58	GND
E3	GND	E4	GND	59	GND	60	PEX_TX13#
1	5V	2	PRSNT_R#	61	PEX_RX13#	62	PEX_TX13
3	5V	4	WAKE#	63	PEX_RX13	64	GND
5	5V	6	PWR_GOOD	65	GND	66	PEX_TX12#
7	5V	8	PWR_EN	67	PEX_RX12#	68	PEX_TX12
9	5V	10	27MHZ_REF	69	PEX_RX12	70	GND
11	GND	12	GND	71	GND	72	PEX_TX11#
13	GND	14	LVSD_U_HPD	73	PEX_RX11#	74	PEX_TX11
15	GND	16	JTAG_TESTEN	75	PEX_RX11	76	GND
17	GND	18	PWR_LEVEL	77	GND	78	PEX_TX10#
19	PEX_STD_SW#	20	TH_OVERT#	79	PEX_RX10#	80	PEX_TX10
21	VGA_DISABLE#	22	TH_ALERT#	81	PEX_RX10	82	GND
23	N/A	24	TH_PWM	83	GND	84	PEX_TX9#
25	N/A	26	GPIO0	85	PEX_RX9#	86	PEX_TX9
27	N/A	28	GPIO1	87	PEX_RX9	88	GND
29	HDMI_CEC	30	GPIO2	89	GND	90	PEX_TX8#
31	LVDS_L_HPD	32	SMB_DAT	91	PEX_RX8#	92	PEX_TX8
33	LVDS_DDC_DAT	34	SMB_CLK	93	PEX_RX8	94	GND
35	LVDS_DDC_CLK	36	GND	95	GND	96	PEX_TX7#
37	GND	38	N/A	97	PEX_RX7#	98	PEX_TX7
39	N/A	40	N/A	99	PEX_RX7	100	GND
41	N/A	42	N/A	101	GND	102	PEX_TX6#
43	N/A	44	N/A	103	PEX_RX6#	104	PEX_TX6
45	N/A	46	GND	105	PEX_RX6	106	GND
47	GND	48	PEX_TX15#	107	GND	108	PEX_TX5#
49	PEX_RX15#	50	PEX_TX15	109	PEX_RX5#	110	PEX_TX5
51	PEX_RX15	52	GND	111	PEX_RX5	112	GND
53	GND	54	PEX_TX14#	113	GND	114	PEX_TX4#
55	PEX_RX14#	56	PEX_TX14	115	PEX_RX4#	116	PEX_TX4

Table 5.2: Connector Pinout(continued)

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
117	PEX_RX4	118	GND	183	LVDS_UTX2	184	LVDS_LTX3
119	GND	120	PEX_TX3#	185	GND	186	GND
121	PEX_RX3#	122	PEX_TX3	187	LVDS_UTX1#	188	LVDS_LTX2#
123	PEX_RX3	124	GND	189	LVDS_UTX1	190	LVDS_LTX2
125	GND	126	KEY	191	GND	192	GND
127	KEY	128	KEY	193	LVDS_UTX0#	194	LVDS_LTX1#
129	KEY	130	KEY	195	LVDS_UTX0	196	LVDS_LTX1
131	KEY	132	KEY	197	GND	198	GND
133	GND	134	GND	199	DP_C_L0#	200	LVDS_LTX0#
135	PEX_RX2#	136	PEX_TX2#	201	DP_C_L0	202	LVDS_LTX0
137	PEX_RX2	138	PEX_TX2	203	GND	204	GND
139	GND	140	GND	205	DP_C_L1#	206	DP_D_L0#
141	PEX_RX1#	142	PEX_TX1#	207	DP_C_L1	208	DP_D_L0
143	PEX_RX1	144	PEX_TX1	209	GND	210	GND
145	GND	146	GND	211	DP_C_L2#	212	DP_D_L1#
147	PEX_RX0#	148	PEX_TX0#	213	DP_C_L2	214	DP_D_L1
149	PEX_RX0	150	PEX_TX0	215	GND	216	GND
151	GND	152	GND	217	DP_C_L3#	218	DP_D_L2#
153	PEX_REFCLK#	154	PEX_CLK_REQ#	219	DP_C_L3	220	DP_D_L2
155	PEX_REFCLK	156	PEX_RST#	221	GND	222	GND
157	GND	158	VGA_DDC_DAT	223	DP_C_AUX#	224	DP_D_L3#
159	JTAG_TDO	160	VGA_DDC_CLK	225	DP_C_AUX	226	DP_D_L3
161	JTAG_TDI	162	VGA_VSYNC	227	RSVD	228	GND
163	JTAG_TCLK	164	VGA_HSYNC	229	RSVD	230	DP_D_AUX#
165	JTAG_TMS	166	GND	231	RSVD	232	DP_D_AUX
167	JTAG_TRST#	168	VGA_RED	233	RSVD	234	DP_C_HPD
169	LVDS_UCLK#	170	VGA_GREEN	235	RSVD	236	DP_D_HPD
171	LVDS_UCLK	172	VGA_BLUE	237	RSVD	238	RSVD
173	GND	174	GND	239	RSVD	240	3V3
175	LVDS_UTX3#	176	LVDS_LCLK#	241	RSVD	242	3V3
177	LVDS_UTX3	178	LVDS_LCLK	243	RSVD	244	GND
179	GND	180	GND	245	RSVD	246	DP_B_L0#
181	LVDS_UTX2#	182	LVDS_LTX3#	247	RSVD	248	DP_B_L0

Table 5.3: Connector Pinout(continued)

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
249	RSVD	250	GND	267	DP_A_L2	268	GND
251	GND	252	DP_B_L1#	269	GND	270	DP_B_AUX#
253	DP_A_L0#	254	DP_B_L1	271	DP_A_L3#	272	DP_B_AUX
255	DP_A_L0	256	GND	273	DP_A_L3	274	DP_B_HPD
257	GND	258	DP_B_L2#	275	GND	276	DP_A_HPD
259	DP_A_L1#	260	DP_B_L2	277	DP_A_AUX#	278	3V3
261	DP_A_L1	262	GND	279	DP_A_AUX	280	3V3
263	GND	264	DP_B_L3#	281	PRSNT_L#	-	
265	DP_A_L2#	266	DP_B_L3				

6.MXM Connector:

Figure 6.1 MXM Connector (Card Top)

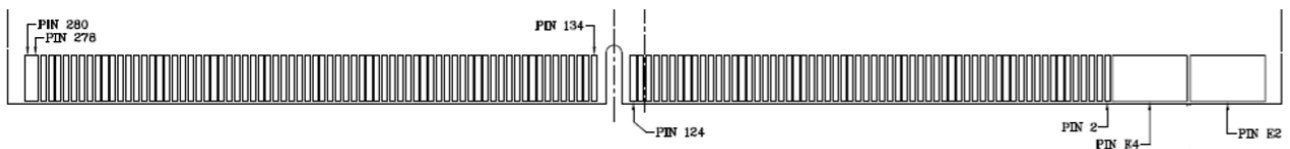
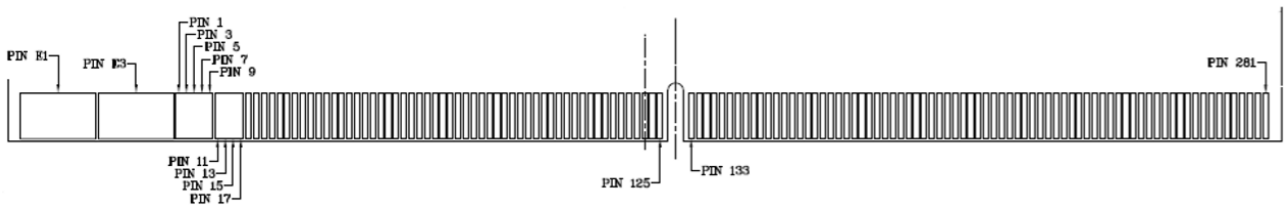


Figure 6.2 MXM Connector (Card Bottom)



7.Power Group:

Table 7.1 shows the MXM module power requirements. The voltage tolerances in the table are specified as measured on module edge finger. The system must be able to supply the full specified current on rails (except PWR_SRC) at all times. The current capability of the PWR_SRC rail must be defined by the system in the MXM system information structure.

Table 7.1: MXM Power Rails

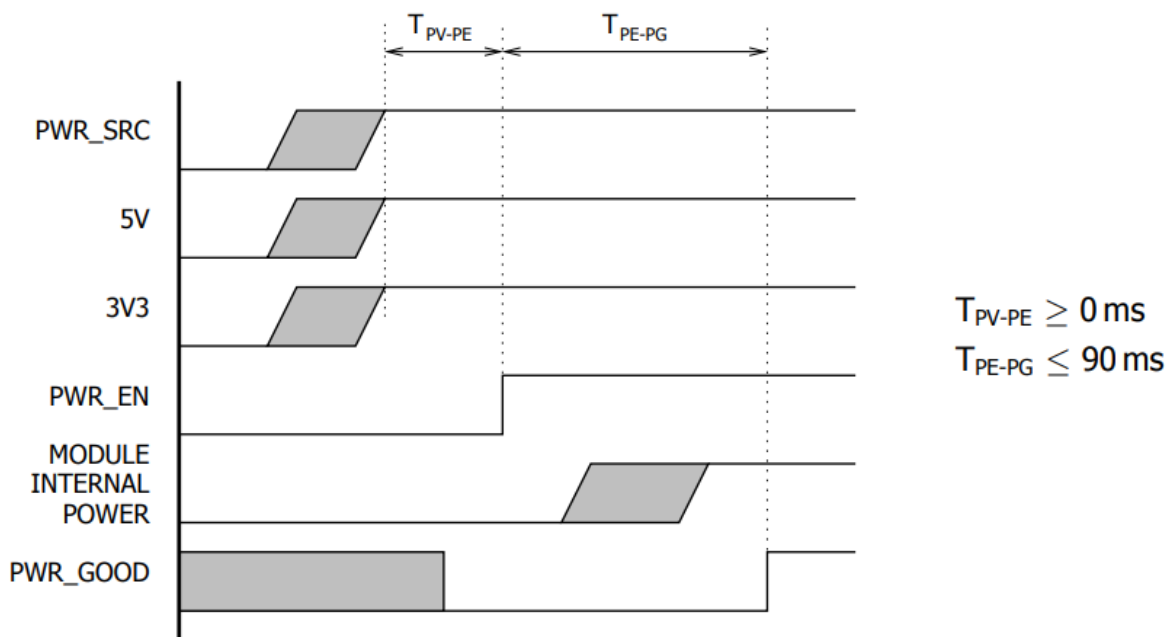
Signal Name	I/O	Type	Impedance	Voltage	Current
PWR_SRC	I	Power	N/A	7-20V	up to 10A
5V	I	Power	N/A	5.0V $\pm$ 6%	2.5A
3.3V	I	Power	N/A	3.3V $\pm$ 6%	2.0A

Note: PWR_SRC voltage range is assumed to be DC or RMS. However under any circumstances the maximum peak voltage shall not exceed 22V and minimum voltage shall not fall below 6.5V.

8.Power Sequencing:

There is no power sequencing requirement for the input voltages to the MXM module. However the PWR_EN signal may be asserted only after all power rails are within specified tolerance. The state of PWR_GOOD is undefined until all rails are fully ramped. Refer to Figure 8.1 for details

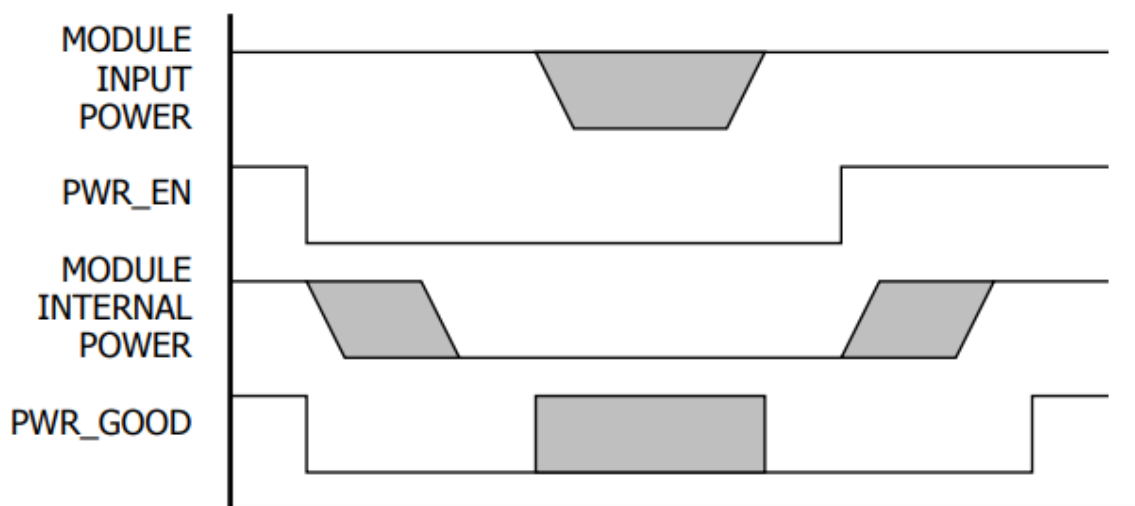
Figure 8.1 Power Sequencing



9. Module Power Down and Power Up:

The MXM module may be powered down using the PWR_EN signal. The system designer may choose to shut down or keep the input power while the module is powered down. Refer to Figure 9.1 for details.

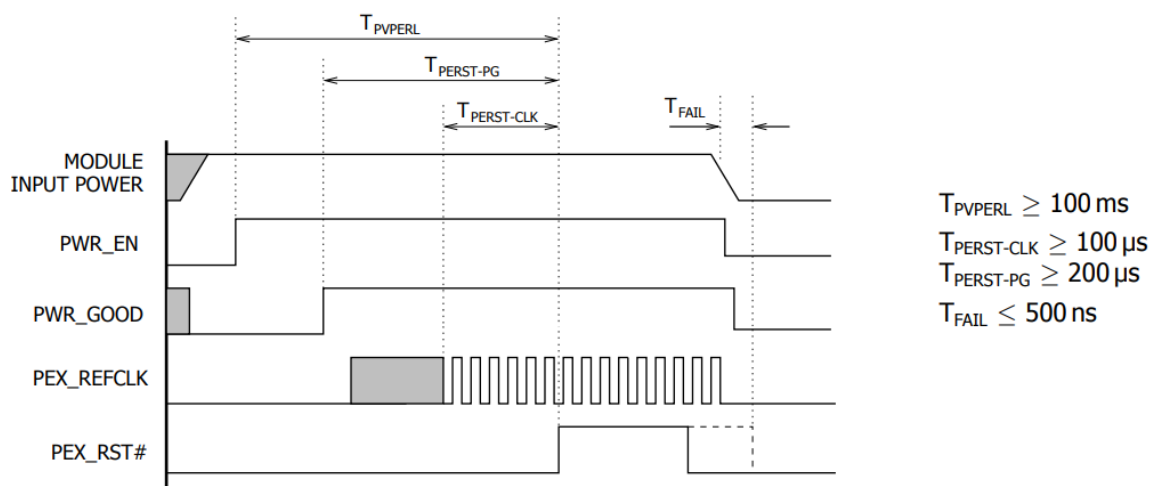
Figure 9.1 Module Power Down



10. Reset Requirements:

System reset may be deasserted only after the assertion of the PWR_GOOD signal. Figure 10.1 shows the reset requirements relative to the PWR_EN and PWR_GOOD signals. This sequence must be followed on initial power on, system reset and resume from suspend/hibernate.

Figure 10.1 Reset Sequencing



11.DVI/HDMI on DP Interface:

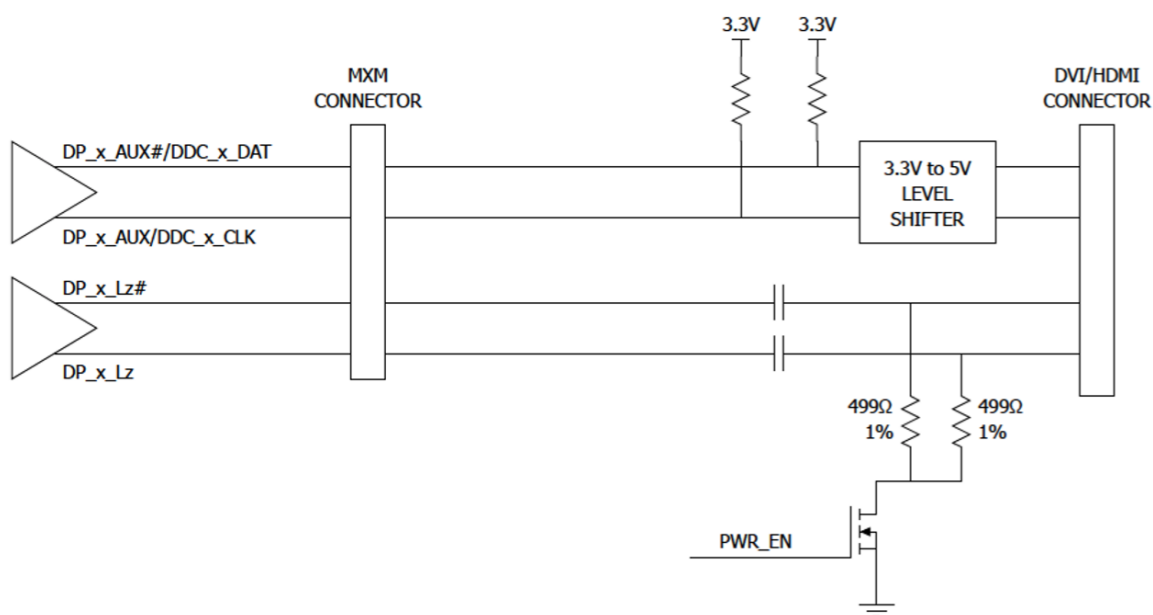
Native DVI or HDMI connector support can be implemented using a DisplayPort interface. Additional circuitry is required on the system and the proper signal mapping must be observed. As Figure 11.1 shows, 499Ω 1% pull-down resistors to ground on the DP lane signals must be placed on the connector side of AC coupling capacitors gated by a FET to limit the leakage. Additionally level shifting circuits must also be implemented on DDC Data and Clock.

Table 11.1 shows the mapping to connect the signals from the MXM connector to the HDMI/DVI connector.

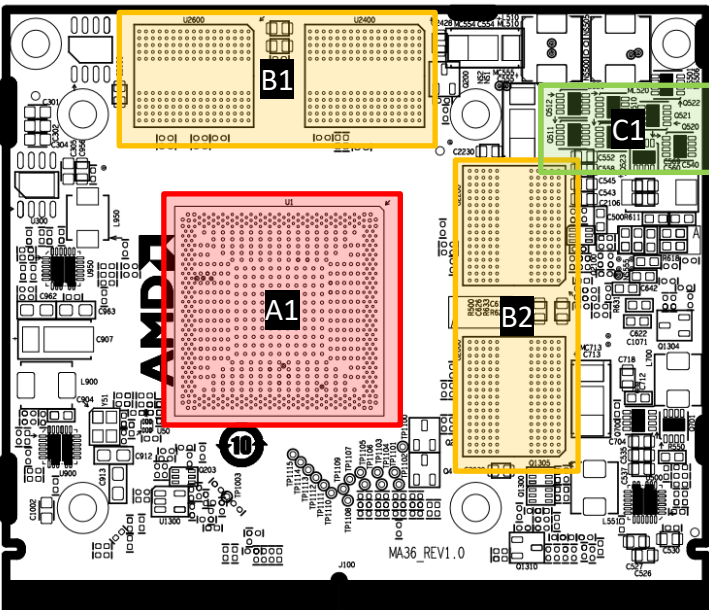
Table 11.1: DisplayPort Multiplexed Signal Definition

Pin Name	DVI/HDMI
DP_x_L0	TX_x_D2
DP_x_L0#	TX_x_D2#
DP_x_L1	TX_x_D1
DP_x_L1#	TX_x_D1#
DP_x_L2	TX_x_D0
DP_x_L2#	TX_x_D0#
DP_x_L3	TX_x_CLK
DP_x_L3#	TX_x_CLK#
DP_x_AUX	DDC_x_CLK
DP_x_AUX#	DDC_x_DAT

Figure 11.1 DVI/HDMI Implementation Using DP Interface



12.Thermal:



Components	Area	TDP	Height
GPU	A1	21W	1.25mm
Memory	B1	1.5W*2	0.9mm
Memory	B2	1.5W*2	0.9mm
Mosfet	C1	2W*8	0.75mm

Chipset		Specification
Dimensions		12*11mm
Stress	Maximum	75 PSI
	Normal	30-40 PSI

Cooling Suggestion:

- 1、显卡建议先固定在散热器上，散热器外径为 4mm，M2 的螺柱。
- 2、散热器的重量要用箱体来支撑固定。
- 3、显存和 MOS 要加导热垫。

Components	壳温满满载限温（环温 45℃）
GPU	95℃
Memory	85℃
Mosfet	105℃

*显存和 MOS 由“探针”监测；GPU 由“Furmark（1920*1080）”监测。

13.Performance:

3Dmark13 FS	FS:5203 FSE:2345 FSU:1145
3Dmark11	E11122 P7574 X1848
3Dmark Vantage	19163
Heaven	561
鲁大师	61025

TechPowerUp GPU-Z 2.52.0

Graphics Card | Sensors | Advanced | Validation

Name: AMD Radeon (TM) E9260 | GPU: Baffin | Revision: 80 | Technology: 14 nm | Die Size: 123 mm² | Release Date: Unknown | Transistors: 3000M | BIOS Version: 015.050.000.001.000000 | Subvendor: AMD/ATI | Device ID: 1002 67E8 - 1002 0B1E | ROPs/TMUs: 16 / 56 | Bus Interface: PCIe x8 3.0 @ x8 3.0 | Shaders: 896 Unified | DirectX Support: 12 (12_0) | Pixel Fillrate: 16.8 GPixel/s | Texture Fillrate: 59.0 GTexel/s | Memory Type: GDDR5 (Micron) | Bus Width: 128 bit | Memory Size: 4096 MB | Bandwidth: 96.0 GB/s | Driver Version: 30.0.14020.2 (Adrenalin 21.40.20) DCH / Win10 64 | Driver Date: Dec 21, 2021 | Digital Signature: WHQL | GPU Clock: 1053 MHz | Memory: 1500 MHz | Shader: N/A | Default Clock: 1053 MHz | Memory: 1500 MHz | Shader: N/A | AMD CrossFire: Disabled | Resizable BAR: Disabled | Computing: ☒ OpenCL ☐ CUDA ☒ DirectCompute ☒ DirectML | Technologies: ☒ Vulkan ☐ Ray Tracing ☐ PhysX ☒ OpenGL 4.6

AIDA64 GPGPU Benchmark

☒ GPU: AMD Radeon (TM) E9260 (Baffin)
1053 MHz, 896 cores, 14 CUs, Driver 3354.13 (PAL,HSAIL)

☒ CPU: 8x 0
3600 MHz, 8 cores, 16 threads

	GPU	x64 CPU
Memory Read	6673 MB/s	21370 MB/s
Memory Write	6393 MB/s	18227 MB/s
Memory Copy	76514 MB/s	20646 MB/s
Single-Precision FLOPS	1860 GFLOPS	293.9 GFLOPS
Double-Precision FLOPS	115.9 GFLOPS	146.9 GFLOPS
24-bit Integer IOPS	1852 GIOPS	208.1 GIOPS
32-bit Integer IOPS	372.0 GIOPS	208.1 GIOPS
64-bit Integer IOPS	92.70 GIOPS	73.54 GIOPS
AES-256	9858 MB/s	3615 MB/s
SHA-1 Hash	18479 MB/s	6466 MB/s
Single-Precision Julia	371.2 FPS	116.1 FPS
Double-Precision Mandel	32.86 FPS	57.03 FPS

AIDA64 v6.20.5300 (c) 1995-2019 FinalWire Ltd.

Save Results Start Benchmark Close

14.Driver:

Windows 10 64bit: <https://drivers.amd.com/drivers/embedded/21.40.20.211221a-375336c-sustaining.zip>

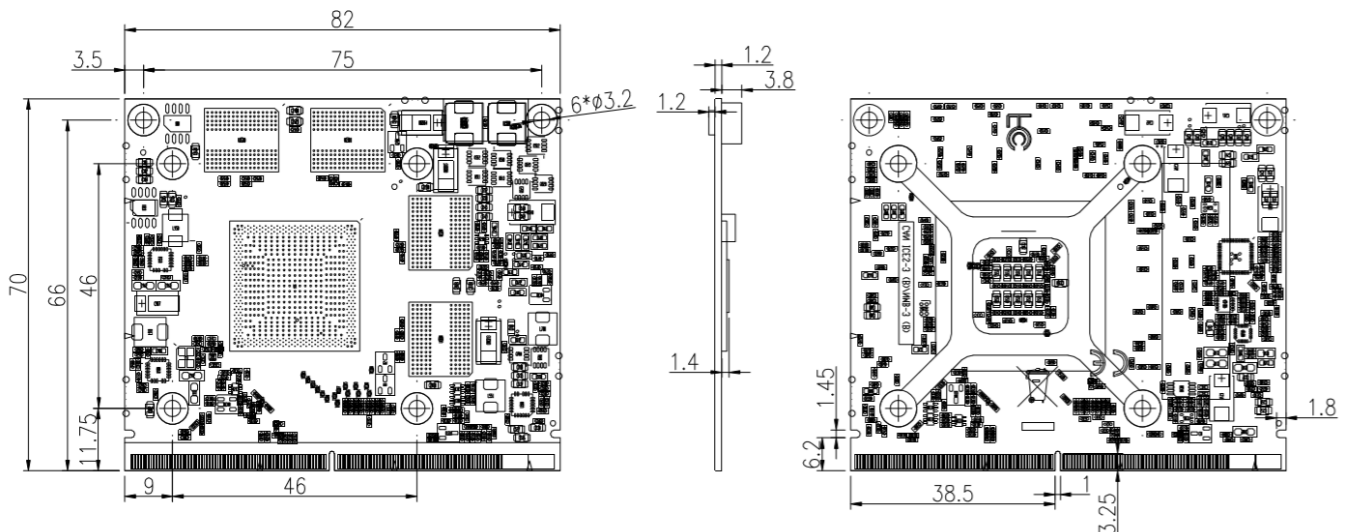
Windows 7 64bit: <https://drivers.amd.com/drivers/embedded/19.50.23.01.200220a-352388C-AES.zip>

Windows 7 32bit: <https://www2.ati.com/drivers/embedded/18.20.28.180731a-332952c-aes.zip>

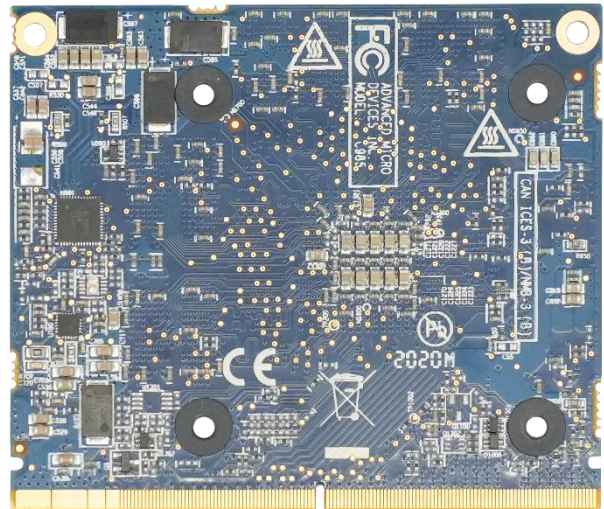
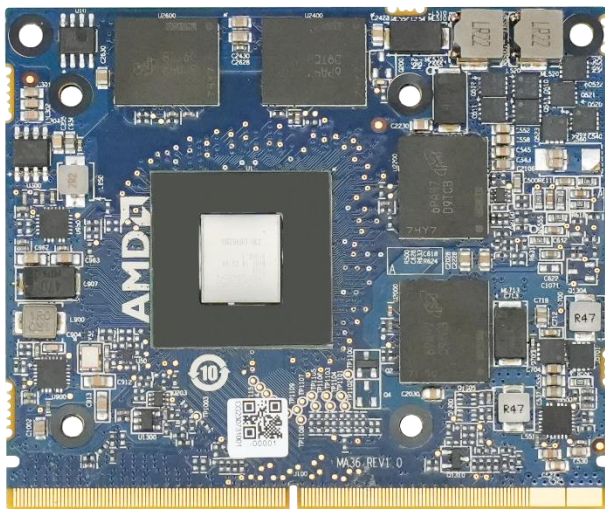
Linux x86 64bit: https://drivers.amd.com/drivers/embedded/amd_ubuntu-20.04.3_kernel_mainline_v2021_40_2375_ga_public.tar.bz2

The latest drive or the drive cannot be downloaded here, please log on the official website to download: <https://www.amd.com/zh-hant/support>

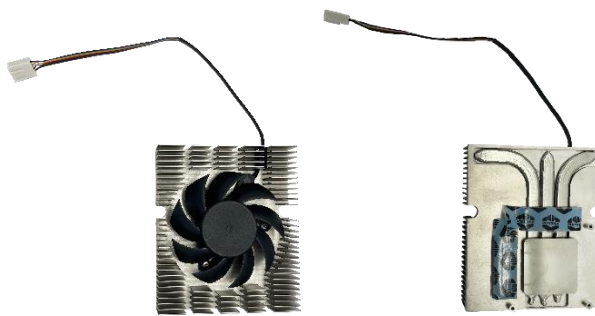
15. Dimensions:



16.Photo:



17.Cooling Option:



1.ZRT.48-6337-00-A00

Dimension	96*80.2*25mm
Rated Speed	4500
Out of Frame Lead Wires	150mm
Connector	4Pin 2.54

18.Question:

故障	排除
不显示 (HDMI 输出)	1、4K 60Hz 显示屏，确认 HDMI 线材是否为 HDMI2.0。 2、将显示器分辨率降为 2K 60Hz。
设备管理员出现惊叹号	1、原系统显卡驱动删除干净。(请使用 Display Driver Uninstaller 删除， https://www.wagnardsoft.com/) 2、Windows 10 版本 16299 不支持，需更新至最新版 (17763、17134 均可)。 3、Windows 7 打补丁 KB2685811。 4、确认核显驱动是否已安装。(不需独立显卡显示) 5、桌面版 GPU 安装在移动平台上，主板 BIOS 需关闭电池管理。
显卡不工作	量下 3V3/5V/2V/Reset 信号是否有电/短路？若有电，显卡就会工作。
显卡无法辨视	检查 PCIE LANE Numbering Reversal 设置。
集显及独显在 Win7 下，无法同时开启 3D 启动	打系统补丁 (KB2685811)，解压后，把所有文件 COPY 到 C 盘 根目录下运行 BAT 文件。

X86 在中标麒麟系统下，显卡驱动后集显输出不能进系统	将 xorg.conf 文件内容清空。
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19.Packing:

Item	N.W(KG)	G.W(KG)	Dimension(mm)	Q'ty(PCS)
Bulk Packing	1.8	3.7	505*340*198	60

20.Part Number:

Model	Part Number	Specification
MD9260A5-4G	8.ZRT.80-6183-26-LFF	MA36 E9260 4G 128bit GDDR5 4HDMI MXM Type A 43W-ZRT 彩包二十八