

1. Specifications:

Model	MD1030A5-2G	
GPU Architecture	NVIDIA Pascal	
Graphics Processing Unit	NVIDIA GeForce GT1030	
NVIDIA CUDA Core	384SP	
Bus Type	MXM3 .1 / up to PCI Express 3.0	
Graphics Clock	1266MHz / 1519MHz (Boost)	
Single Precision FLOPS	1346GFLOPS	
Double Precision FLOPS	42.58GFLOPS	
Memory Size	2GB 64bit GDDR5	
Memory Clock	1502MHz (6.0 Gbps)	
Display Features	DP A: HDMI 2.0b DP B: DVI	DP A: Display Port1.4++ DP B: HDMI 2.0b
Max Resolution	DP:7680x4320@120Hz HDMI:3840x2160@60Hz DVI:1920x1080@60Hz	
Board Power	45W (Option 32W)	
Board Dimensions	MXM Graphics Module Version 3.1 Type A (70x82mm)	
Number of output Channel	2	
Operation System	Windows 7/8/8.1/10 32/64bit 、 Linux 32/64bit	
VIN Range	DC 12~19V, 3.3V & 5V; +/-5%	

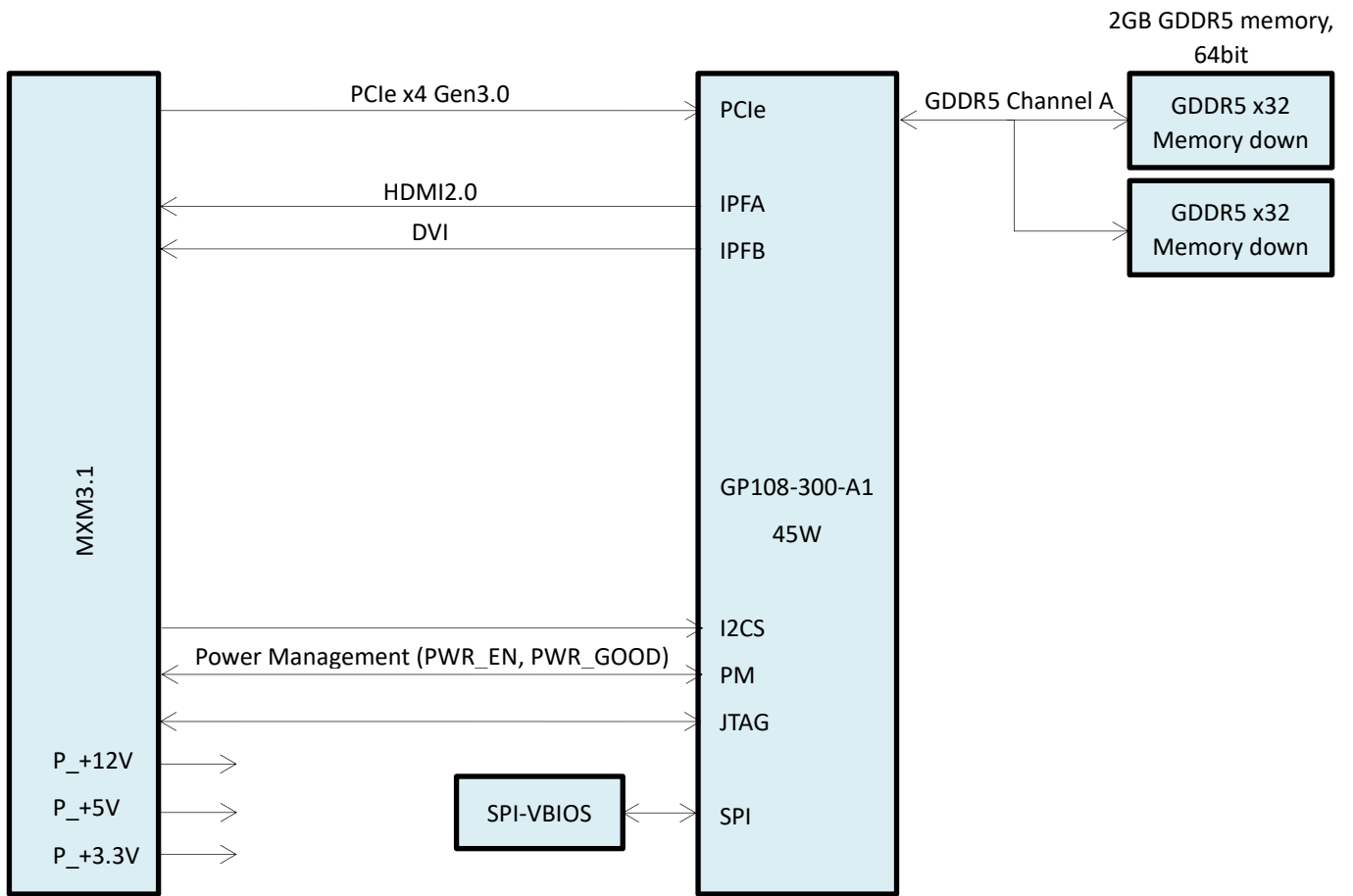
OpenGL	4.6
Open CL	1.2
DirectX	12
Video Playback	H.265, VC1, MPEG2 1080P

Operating Temperature	0~45°C (Option -30°C)
Storage Temperature	-20~75°C
Operating Humidity	0~95% (non-condensing)
Storage Humidity	10~90%

Surround (Landscape)	Surround (Portrait)
2x1(3840x1080@60Hz)	2x1(2160x1920@60Hz)
1x2(1920x2160@60Hz)	1x2(1080x3840@60Hz)

**Total resolution based on every display resolution is 1920*1080@60Hz.*

2. Block Diagram:



3. MXM Board Outlines:

Figure 3.1 shows the board outlines (top side view) for Type A MXM modules. An additional system keep-out of 0.5mm[0.020] per side is allowed on the PCB to accommodate whatever means of production panelization is required. This additional clearance is above and beyond the dimensional limits presented here. The location of these features is not specified or controlled.

Figure 3.1: Board Outlines

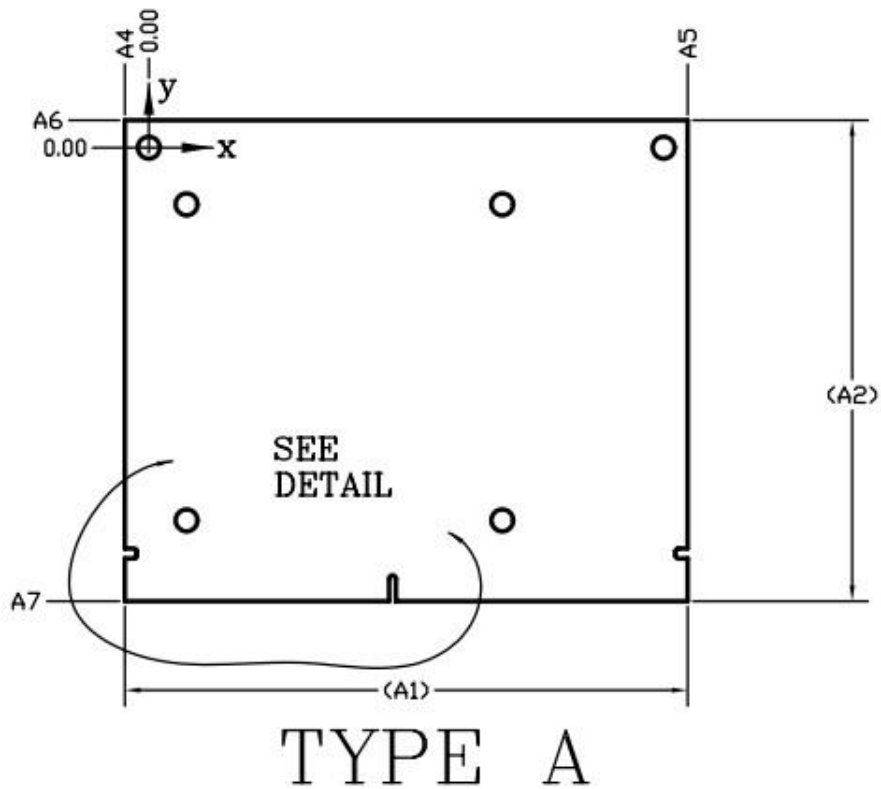


Table 3.1: Board Outline Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
A1		82.00			3.228	
A2		70.00			2.756	
A3		105.00			4.134	
A4	3.37	3.50	3.63	0.133	0.138	0.143
A5	78.37	78.50	78.63	3.085	3.091	3.096
A6	3.87	4.00	4.13	0.152	0.157	0.163
A7	65.87	66.00	66.13	2.593	2.598	2.604
A8	38.87	39.00	39.13	1.530	1.535	1.541

Figure 3.2: Board Slots Detail

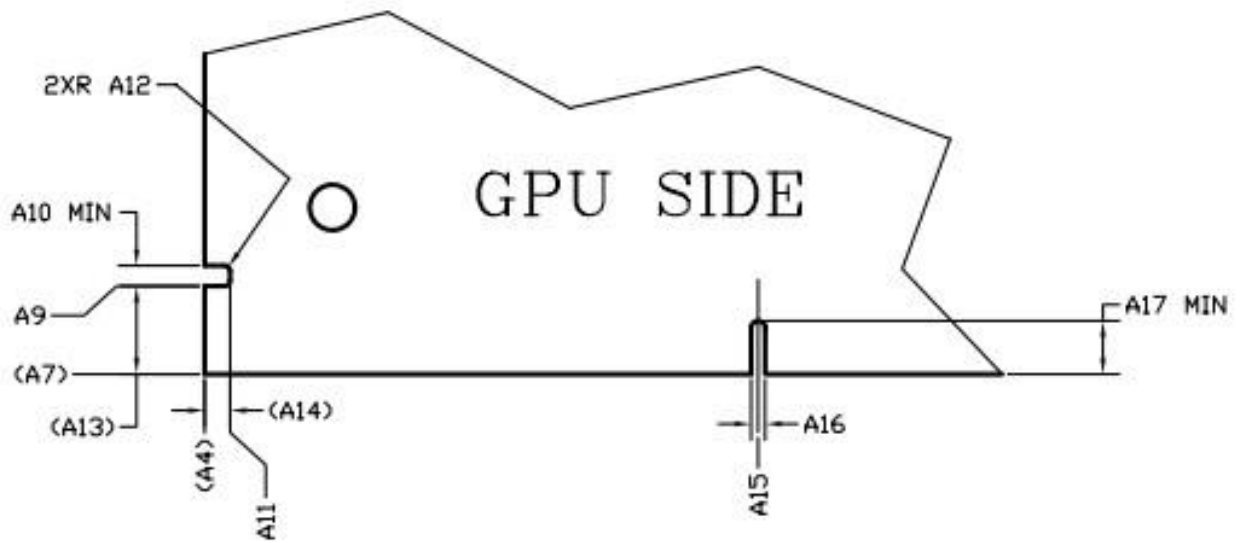


Table 3.2: Board Slots Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
A4		3.50			0.138	
A7		66.00			2.598	
A9	59.67	59.80	59.93	2.349	2.354	2.359
A10	1.45			0.057		
A11	1.57	1.70	1.83	0.062	0.067	0.072
A12	0.32	0.50	0.58	0.013	0.020	0.023
A13		6.20			0.244	
A14		1.80			0.071	
A15	35.37	35.50	35.63	1.393	1.398	1.403
A16	0.95	1.00	1.05	0.037	0.039	0.041
A17	3.75			0.148		

4.MXM PCB Mounting Holes:

All MXM version 3.1 modules have 6 holes. Two are used to secure the board to the system and the other four to fasten the thermal solution to the module.

Figure 4.1: Mounting Holes

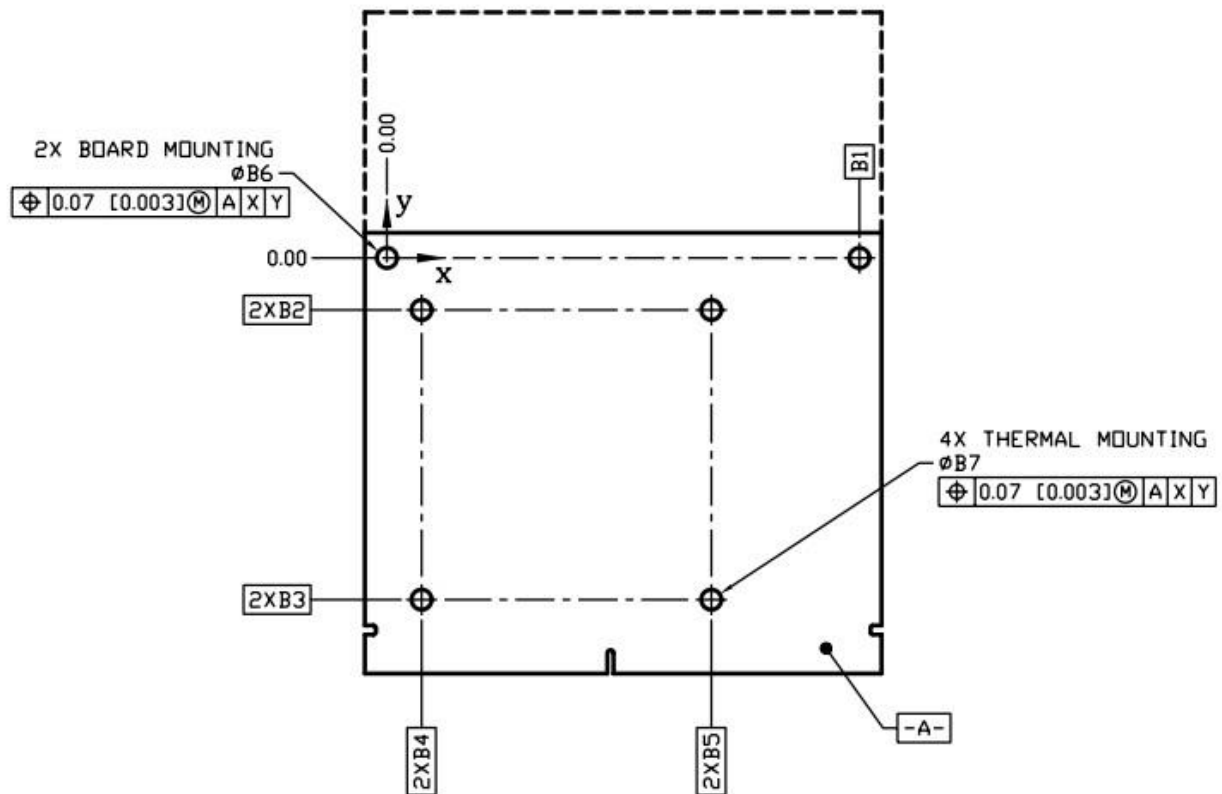


Table 4.1: Mounting Holes Dimensions

Symbol	[mm]			[in]		
	min	nom	max	min	nom	max
B1		75.00			2.953	
B2		8.25			0.325	
B3		54.25			2.136	
B4		5.50			0.217	
B5		51.50			2.028	
B6	3.07	3.20	3.33	0.121	0.126	0.131
B7	3.07	3.20	3.33	0.121	0.126	0.131

5.Connector Pinout:

Table 5.1, Table 5.2 and Table 5.3 list the connector pinout.

Table 5.1: Connector Pinout

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
E1	PWR_SRC	E2	PWR_SRC	59	GND	60	N/A
E3	GND	E4	GND	61	N/A	62	N/A
1	5V	2	PRSNT_R	63	N/A	64	GND
3	5V	4	WAKE	65	GND	66	N/A
5	5V	6	PWR_GOOD	67	N/A	68	N/A
7	5V	8	PWR_EN	69	N/A	70	GND
9	5V	10	27MHZ_REF	71	GND	72	N/A
11	GND	12	GND	73	N/A	74	N/A
13	GND	14		75	N/A	76	GND
15	GND	16	RSVD	77	GND	78	N/A
17	GND	18	PWR_LEVEL	79	N/A	80	N/A
19	PEX_STD_SW#	20	TH_OVERT#	81	N/A	82	GND
21	VGA_DISABLE#	22	TH_ALERT#	83	GND	84	N/A
23	N/A	24	TH_PWM	85	N/A	86	N/A
25	N/A	26	GPIO0	87	N/A	88	GND
27	N/A	28	GPIO1	89	GND	90	N/A
29	HDMI_CEC	30	GPIO2	91	N/A	92	N/A
31	N/A	32	SMB_DAT	93	N/A	94	GND
33	N/A	34	SMB_CLK	95	GND	96	N/A
35	N/A	36	GND	97	N/A	98	N/A
37	GND	38	OEM0	99	N/A	100	GND
39	OEM1	40	OEM2	101	GND	102	N/A
41	OEM3	42	OEM4	103	N/A	104	N/A
43	OEM5	44	OEM6	105	N/A	106	GND
45	OEM7	46	GND	107	GND	108	N/A
47	GND	48	N/A	109	N/A	110	N/A
49	N/A	50	N/A	111	N/A	112	GND
51	N/A	52	GND	113	GND	114	N/A
53	GND	54	N/A	115	N/A	116	N/A
55	N/A	56	N/A	117	N/A	118	GND
57	N/A	58	GND	119	GND	120	PEX_TX3#

Table 5.2: Connector Pinout(continued)

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
121	PEX_RX3#	122	PEX_TX3	191	GND	192	GND
123	PEX_RX3	124	GND	193	N/A	194	N/A
125	GND	126	KEY	195	N/A	196	N/A
127	KEY	128	KEY	197	GND	198	GND
129	KEY	130	KEY	199	N/A	200	N/A
131	KEY	132	KEY	201	N/A	202	N/A
133	GND	134	GND	203	GND	204	GND
135	PEX_RX2#	136	PEX_TX2#	205	N/A	206	N/A
137	PEX_RX2	138	PEX_TX2	207	N/A	208	N/A
139	GND	140	GND	209	GND	210	GND
141	PEX_RX1#	142	PEX_TX1#	211	N/A	212	N/A
143	PEX_RX1	144	PEX_TX1	213	N/A	214	N/A
145	GND	146	GND	215	GND	216	GND
147	PEX_RX0#	148	PEX_TX0#	217	N/A	218	N/A
149	REX_RX0	150	PEX_TX0	219	N/A	220	N/A
151	GND	152	GND	221	GND	222	GND
153	PEX_REFCLK#	154	PEX_CLK_REQ	223	N/A	224	N/A
155	PEX_REFCLK	156	PEX_RST#	225	N/A	226	N/A
157	GND	158	N/A	227	RSVD	228	GND
159	JTAG_TDO	160	N/A	229	RSVD	230	N/A
161	JTAG_TDI	162	N/A	231	RSVD	232	N/A
163	JTAG_TCLK	164	N/A	233	RSVD	234	N/A
165	JTAG_TMS	166	GND	235	RSVD	236	N/A
167	JTAG_TRST	168	N/A	237	RSVD	238	RSVD
169	N/A	170	N/A	239	RSVD	240	3V3
171	N/A	172	N/A	241	RSVD	242	3V3
173	GND	174	GND	243	RSVD	244	GND
175	N/A	176	N/A	245	RSVD	246	DP_B_L0#
177	N/A	178	N/A	247	RSVD	248	DP_B_L0
179	GND	180	GND	249	RSVD	250	GND
181	N/A	182	N/A	251	GND	252	DP_B_L1#
183	N/A	184	N/A	253	DP_A_L0#	254	DP_B_L1
185	GND	186	GND	255	DP_A_L0	256	GND
187	N/A	188	N/A	257	GND	258	DP_B_L2#
189	N/A	190	N/A	259	DP_A_L1#	260	DP_B_L2

Table 5.3: Connector Pinout(continued)

Pin	Signal Name	Pin	Signal Name	Pin	Signal Name	Pin	Signal Name
261	DP_A_L1	262	GND	273	DP_A_L3	274	DP_B_HPD
263	GND	264	DP_B_L3#	275	GND	276	DP_A_HPD
265	DP_A_L2#	266	DP_B_L3	277	DP_A_AUX#	278	3V3
267	DP_A_L2	268	GND	279	DP_A_AUX	280	3V3
269	GND	270	DP_B_AUX#	281	PRSNT_L	-	
271	DP_A_L3#	272	DP_B_AUX				

6.MXM Connector:

Figure 6.1 MXM Connector (Card Top)

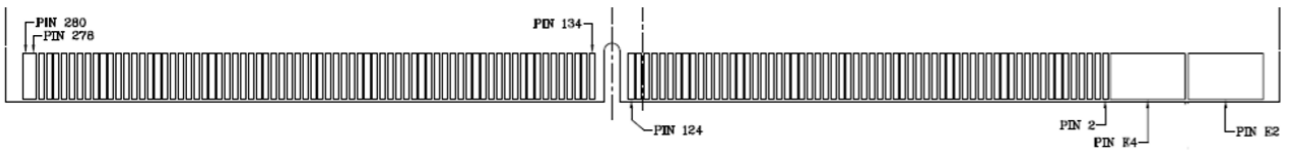
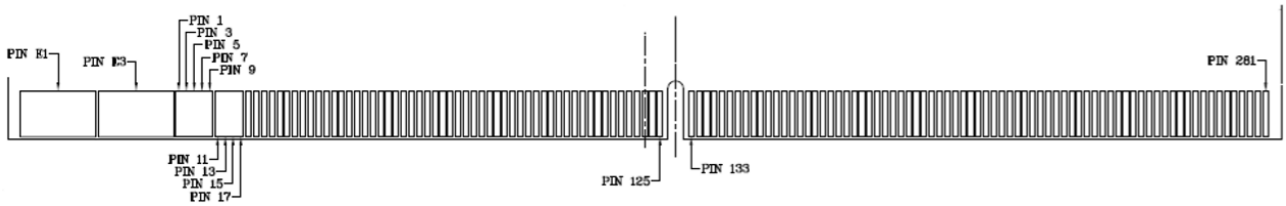


Figure 6.2 MXM Connector (Card Bottom)



7. Power Group:

Table 7.1 shows the MXM module power requirements. The voltage tolerances in the table are specified as measured on module edge finger. The system must be able to supply the full specified current on rails (except PWR_SRC) at all times. The current capability of the PWR_SRC rail must be defined by the system in the MXM system information structure.

Table 7.1: MXM Power Rails

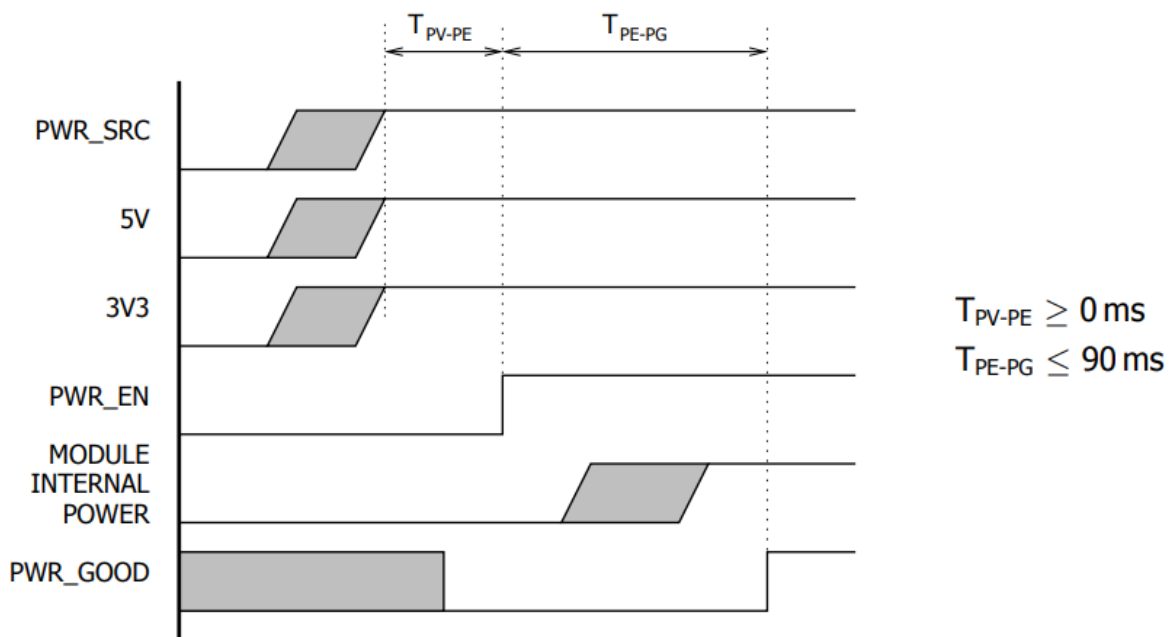
Signal Name	I/O	Type	Impedance	Voltage	Current
PWR_SRC	I	Power	N/A	7-20V	up to 10A
5V	I	Power	N/A	5.0V \pm 6%	2.5A
3.3V	I	Power	N/A	3.3V \pm 6%	2.0A

Note: PWR_SRC voltage range is assumed to be DC or RMS. However under any circumstances the maximum peak voltage shall not exceed 22V and minimum voltage shall not fall below 6.5V.

8. Power Sequencing:

There is no power sequencing requirement for the input voltages to the MXM module. However the PWR_EN signal may be asserted only after all power rails are within specified tolerance. The state of PWR_GOOD is undefined until all rails are fully ramped. Refer to Figure 8.1 for details

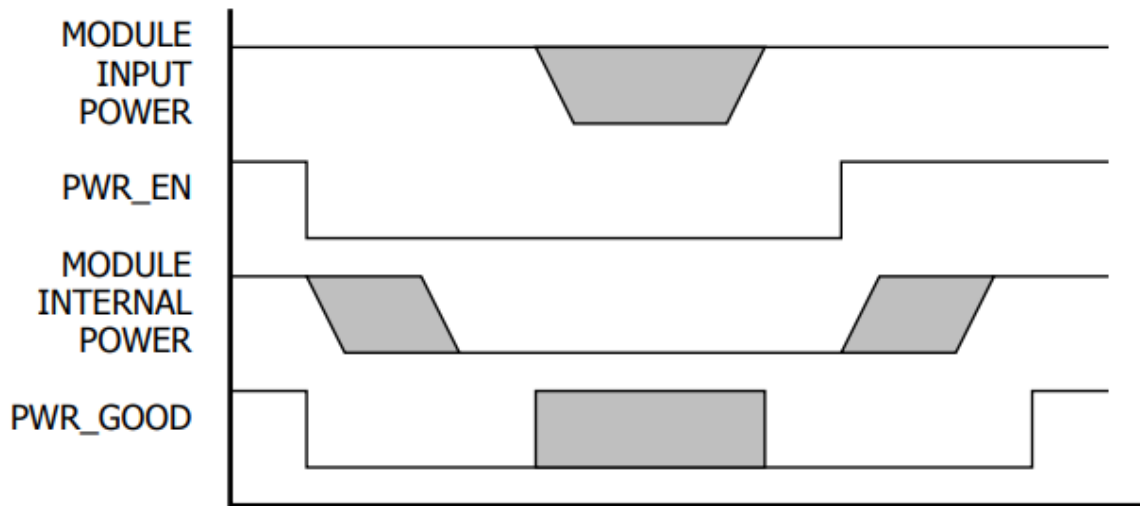
Figure 8.1 Power Sequencing



9. Module Power Down and Power Up:

The MXM module may be powered down using the PWR_EN signal. The system designer may choose to shut down or keep the input power while the module is powered down. Refer to Figure 9.1 for details.

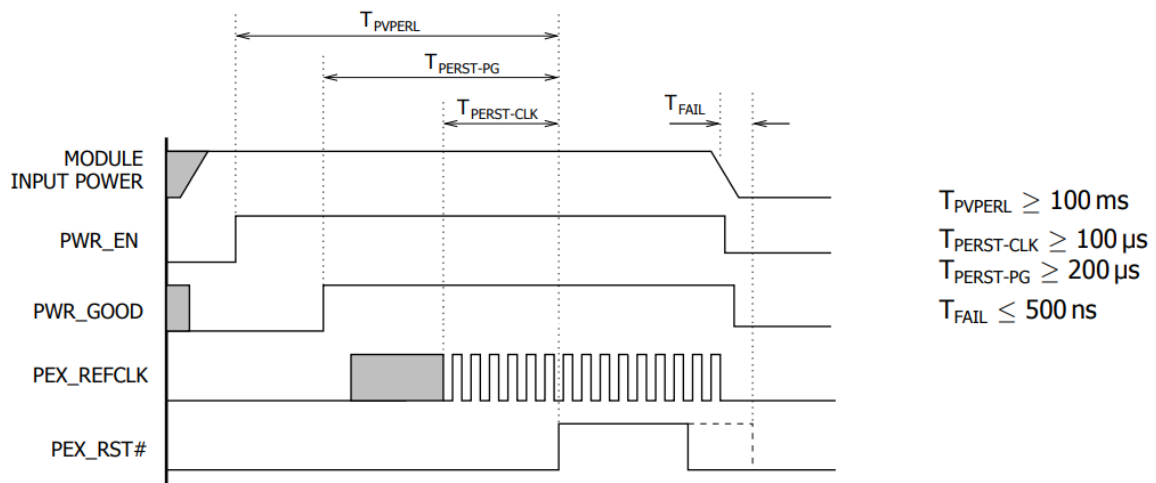
Figure 9.1 Module Power Down



10. Reset Requirements:

System reset may be deasserted only after the assertion of the PWR_GOOD signal. Figure 10.1 shows the reset requirements relative to the PWR_EN and PWR_GOOD signals. This sequence must be followed on initial power on, system reset and resume from suspend/hibernate.

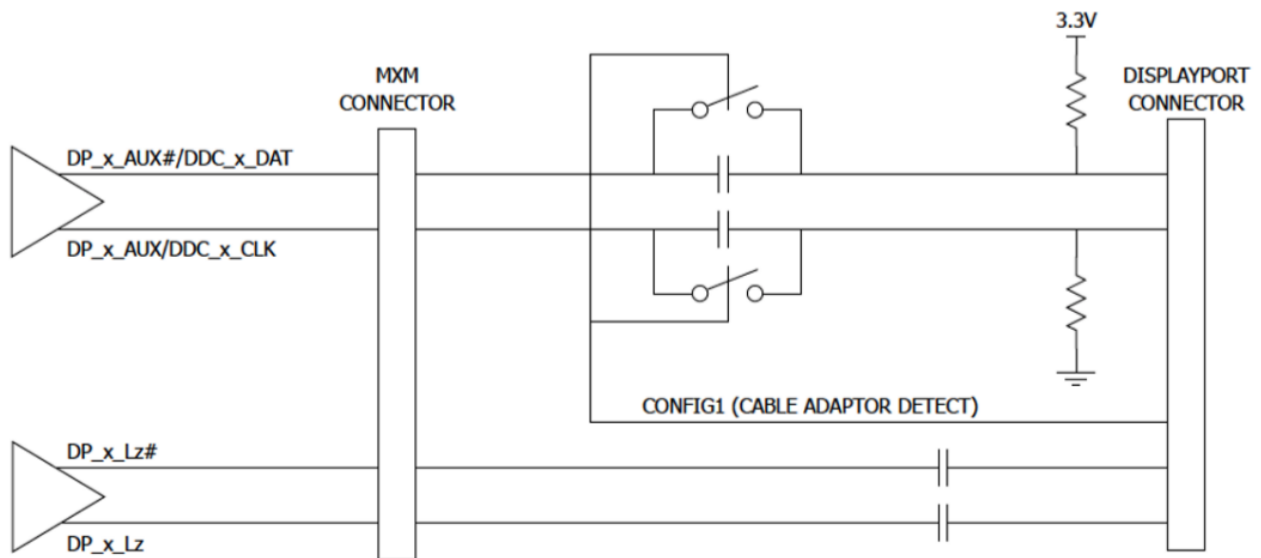
Figure 10.1 Reset Sequencing



11.DisplayPort Interface:

DC blocking capacitors of DisplayPort must be placed on the system board. In addition to the MXM implementation of Dual-mode DisplayPort requires the circuit in Figure 11.1 on the AUX lines for proper dongle detection. The HPD signal conditioning must also be placed on the system board.

Figure 11.1 Dual-mode DisplayPort Implementation



12.DVI/HDMI on DP Interface:

Native DVI or HDMI connector support can be implemented using a DisplayPort interface.

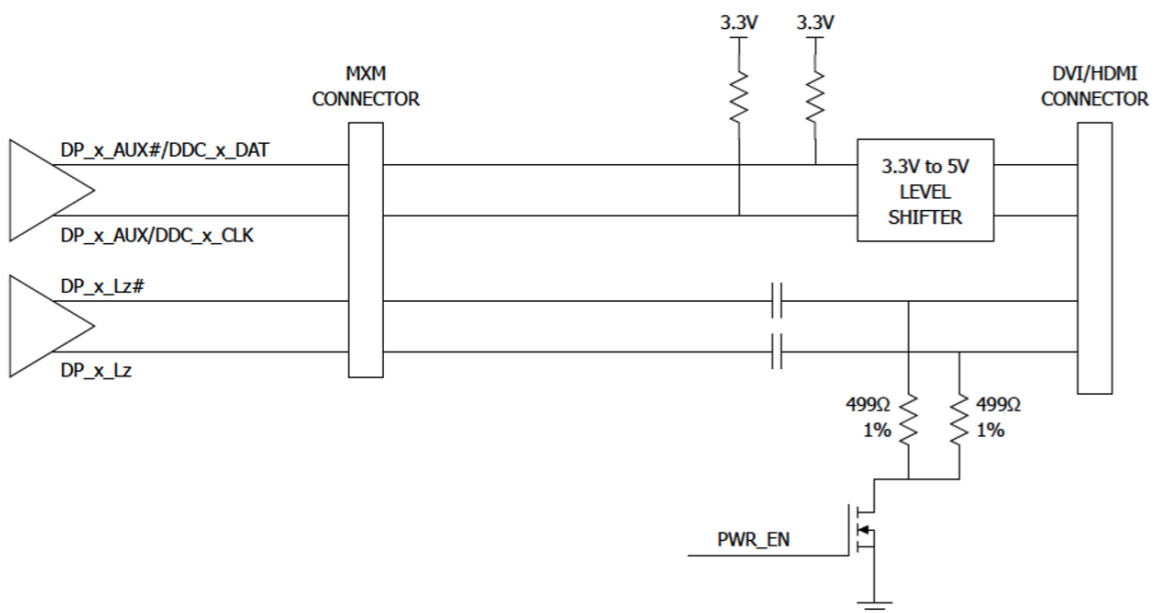
Additional circuitry is required on the system and the proper signal mapping must be observed. As Figure 12.1 shows, 499Ω 1% pull-down resistors to ground on the DP lane signals must be placed on the connector side of AC coupling capacitors gated by a FET to limit the leakage. Additionally level shifting circuits must also be implemented on DDC Data and Clock.

Table 12.1 shows the mapping to connect the signals from the MXM connector to the HDMI/DVI connector.

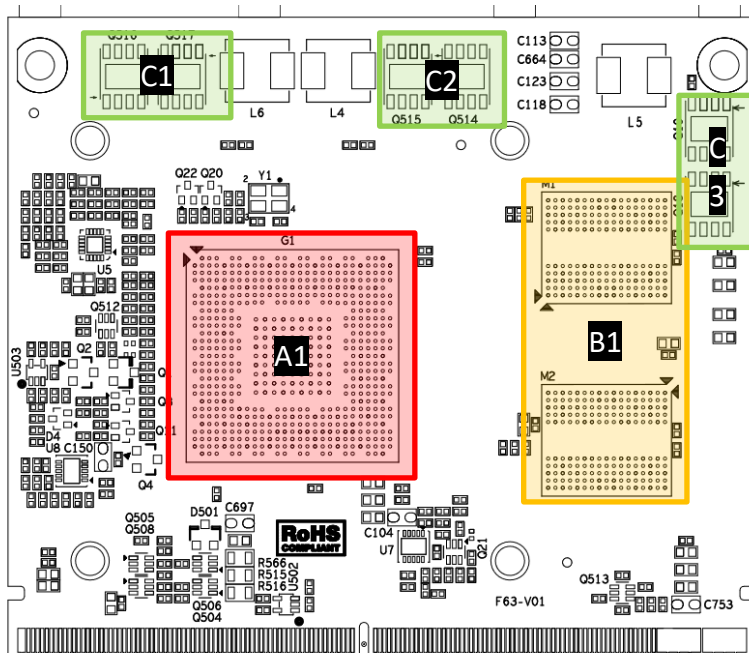
Table 12.1: DisplayPort Multiplexed Signal Definition

Pin Name	DVI/HDMI
DP_xL0	TX_x_D2
DP_xL0#	TX_x_D2#
DP_x_L1	TX_x_D1
DP_x_L1#	TX_x_D1#
DP_x_L2	TX_x_D0
DP_x_L2#	TX_x_D0#
DP_x_L3	TX_x_CLK
DP_x_L3#	TX_x_CLK#
DP_x_AUX	DDC_x_CLK
DP_x_AUX#	DDC_x_DAT

Figure 12.1 DVI/HDMI Implementation Using DP Interface



13.Thermal:



Components	Area	TDP	Height
GPU	A1	30W	1.7mm
Memory	B1	1.5W*2	0.9mm
Mosfet	C1、C2、C3	2W*6	0.75mm

Chipset		Specification
Dimensions		9.5*8mm
Stress	Maximum	75 PSI
	Normal	30-40 PSI

Cooling Suggestion:

- 1、显卡建议先固定在散热器上，散热器外径为 4mm，M2 的螺柱。
- 2、散热器的重量要用箱体来支撑固定。
- 3、显存和 MOS 要加导热垫。

Components	满载限温 (环温 45℃)
GPU	95℃
Memory	85℃
Mosfet	105℃

*显存和 MOS 由“探针”监测；GPU 由“Furmark (1920*1080)”监测。

14.Performance:

3Dmark13	3344	Cuda	
3Dmark11	P5100 X1669	single kernels	537.679 / 32.2238
3Dmark Vantage	11674	N=10 w/o streams	812.455 / 32.6694
Heaven	1452	N=10 with streams	1018.73 / 36.4494
鲁大师	45795	N=10 batched	978.363 / 38.9105

TechPowerUp GPU-Z 2.30.0

Graphics Card | Sensors | Advanced | Validation

Name: NVIDIA GeForce GT 1030 [Lookup]

GPU: GP108 | Revision: A1

Technology: 14 nm | Die Size: 71 mm²

Release Date: Feb 5, 2017 | Transistors: 1800M

BIOS Version: 86.08.0C.00.0B [UEFI]

Subvendor: MSI | Device ID: 10DE 1D01 - 1462 8C98

ROPs/TMUs: 16 / 24 | Bus Interface: PCIe x4 3.0 @ x4 3.0

Shaders: 384 Unified | DirectX Support: 12 (12_1)

Pixel Fillrate: 24.3 GPixel/s | Texture Fillrate: 36.5 GTexel/s

Memory Type: GDDR5 (Samsung) | Bus Width: 64 bit

Memory Size: 2048 MB | Bandwidth: 48.1 GB/s

Driver Version: 26.21.14.4259 (NVIDIA 442.59) DCH / Win10 64

Driver Date: Mar 04, 2020 | Digital Signature: WHQL

GPU Clock: 1266 MHz | Memory: 1502 MHz | Boost: 1519 MHz

Default Clock: 1266 MHz | Memory: 1502 MHz | Boost: 1519 MHz

NVIDIA SLI: Disabled

Computing: OpenCL CUDA DirectCompute DirectML

Technologies: Vulkan Ray Tracing PhysX OpenGL 4.6

NVIDIA GeForce GT 1030 [Close]

AIDA64 GPGPU Benchmark

GPU: nVIDIA GeForce GT 1030 (GP108)
1518 MHz, 384 cores, 3 CUs, Driver 391.35

CPU: Intel Core i5 (Kaby Lake-S)
4000 MHz, 6 cores, 6 threads

GPU

Memory Read	3191 MB/s	
Memory Write	2903 MB/s	
Memory Copy	38113 MB/s	
Single-Precision FLOPS	1346 GFLOPS	
Double-Precision FLOPS	42.58 GFLOPS	
24-bit Integer IOPS	449.6 GIOPS	
32-bit Integer IOPS	451.2 GIOPS	
64-bit Integer IOPS	76.85 GIOPS	
AES-256	3575 MB/s	
SHA-1 Hash	7632 MB/s	
Single-Precision Julia	217.6 FPS	
Double-Precision Mandel	13.25 FPS	

AIDA64 v5.90.4200 (c) 1995-2017 FinalWire Ltd.

[Save] [Results] [Start Benchmark] [Close]

15.Driver:

Windows 10 64bit: <https://cn.download.nvidia.com/Windows/466.77/466.77-desktop-win10-64bit-international-dch-whql.exe>

Windows 8.1 64bit: <https://cn.download.nvidia.com/Windows/466.77/466.77-desktop-win8-win7-64bit-international-whql.exe>

Windows 8 64bit: <https://cn.download.nvidia.com/Windows/466.77/466.77-desktop-win8-win7-64bit-international-whql.exe>

Windows 7 64bit: <https://cn.download.nvidia.com/Windows/466.77/466.77-desktop-win8-win7-64bit-international-whql.exe>

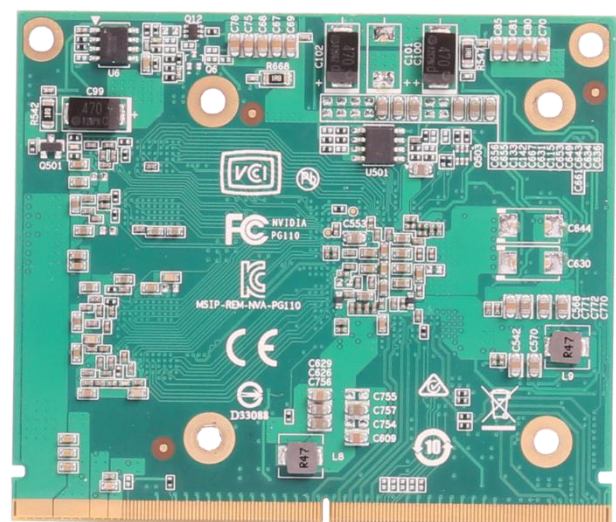
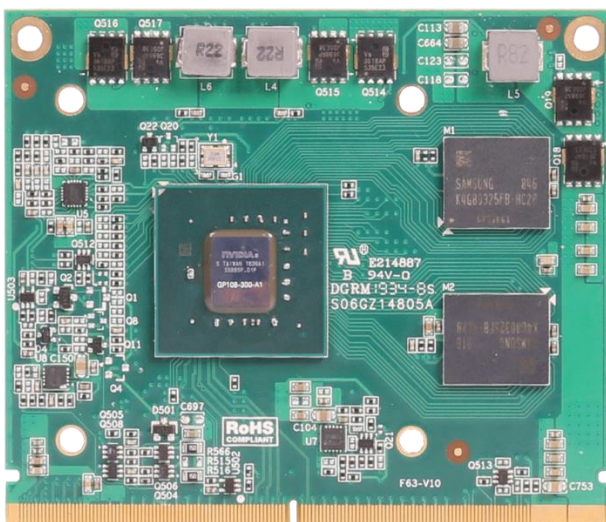
Linux Aarch64: <https://cn.download.nvidia.com/XFree86/aarch64/460.84/NVIDIA-Linux-aarch64-460.84.run>

Linux 64bit: https://cn.download.nvidia.com/XFree86/Linux-x86_64/460.84/NVIDIA-Linux-x86_64-460.84.run

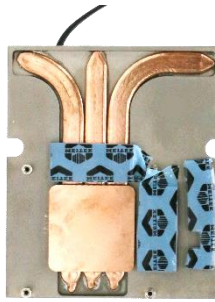
FreeBSD x64: https://cn.download.nvidia.com/XFree86/FreeBSD-x86_64/460.84/NVIDIA-FreeBSD-x86_64-460.84.tar.gz

The latest drive or the drive cannot be downloaded here, please log on the official website to download: <https://www.nvidia.cn/geforce/drivers/>

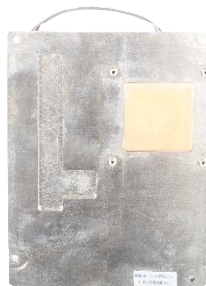
16.Photo:



17.Cooling Option:



1.ZRT.48-6209-00-A00	
Dimension	96*80.2*25mm
Rated Speed	4500
Out of Frame Lead Wires	150mm
Connector	4Pin 2.54



1.ZRT.48-6140-00-A00	
Dimension	133*103*26.5mm
Rated Speed	3800
Out of Frame Lead Wires	100mm
Connector	4Pin 2.54

18.Certificate:



SHENZHEN HTT TECHNOLOGY CO.,LTD.
HOTLINE: 400-6655-351
WWW.HTTPRC.COM

Certificate of Conformity

Certificate No. : HTT202009359E
Applicant : Shenzhen Zrt Co. Ltd.
Applicant Address : Room B707, 7/F, juzhou electric appliance building, 12th south keji road, nanshan district, shenzhen
Manufacturer : Shenzhen Zrt Co. Ltd.
Manufacturer Address : Room B707, 7/F, juzhou electric appliance building, 12th south keji road, nanshan district, shenzhen
Product : Graphics Card
Model No. : MD1030A5-2G, MD1030A5-2G(02)
Trademark : N/A

The following products have been tested by us with listed standards and found in compliance with the council EMC 2014/30/EU. It is possible to use CE marking to demonstrate the compliance with this EMC.

Test standards	Report(s) Number	Issued By	Issued Date
EN 55032:2015+A1+C:2016+A11:2020 EN 55035:2017+A11:2020 EN IEC 61000-3-2:2019 EN 61000-3-3:2013/A1:2019	HTT202009359E	HTT	Oct.09.2020

This certificate of conformity is not transferable and based on an evaluation of a sample of the above mentioned product.



Authorized Signer: *Kevin Yang* 

Kevin Yang/Senior Manager

Date: Oct.09.2020

TEL: 0755-23595200 FAX: 0755-23595201
1F, B BUILDING, HUAFENG INTERNATIONAL ROBOTICS INDUSTRIAL PARK, GUSHU, XIXIANG STREET, BAO'AN DISTRICT, SHENZHEN



SHENZHEN HTT TECHNOLOGY CO.,LTD.
HOTLINE: 400-6655-351
WWW.HTTPRC.COM

FCC-VERIFICATION

HTT202009359F
This verification that the following designated product
Graphics Card
Model No.: MD1030A5-2G, MD1030A5-2G(02)
 (Product Identification)

It is herewith confirmed and found to comply with the requirements set up by regulation for the evaluation of electromagnetic compatibility. This device complies with the FCC rules; operation is subject to the following two conditions:
 (1) this device may not cause harmful interference and.
 (2) this device must accept any interference received, including interference that may cause undesired operation

This declaration is the responsibility of the manufacturer/importer
Shenzhen Zrt Co. Ltd.
Room B707, 7/F, juzhou electric appliance building,
12th south keji road, nanshan district, Shenzhen

Identification of regulation/standards: FCC Part 15 Subpart B Class B, Measurement Procedure: ANSI C63.4-2014. It is only valid in connection with the test report number: **HTT202009359FR**

TEST LABORATORY

This is the results of test that was carried out from the submitted type-samples of a product in conformity with the specification of the respective standards. The certificate holder has the right to fix the FCC-mark for EMI on the product complying with the inspection sample.



Authorized Signer: *Kevin Yang* 

Kevin Yang/Senior Manager

Date: Oct.09.2020

This verification is only valid for the equipment and configuration described, in conjunction with the test data detailed above

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1F, B BUILDING, HUAFENG INTERNATIONAL ROBOTICS INDUSTRIAL PARK, GUSHU, XIXIANG STREET, BAO'AN DISTRICT, SHENZHEN



SHENZHEN HTT TECHNOLOGY CO.,LTD.
HOTLINE: 400-6655-351
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Certificate of Conformity

Certificate No. : HTT202009359R
Applicant : Shenzhen Zrt Co. Ltd.
Applicant Address : Room B707, 7/F, juzhou electric appliance building, 12th south keji road, nanshan district, shenzhen
Manufacturer : Shenzhen Zrt Co. Ltd.
Manufacturer Address : Room B707, 7/F, juzhou electric appliance building, 12th south keji road, nanshan district, shenzhen
Product : Graphics Card
Model No. : MD1030A5-2G, MD1030A5-2G(02)
Trademark : N/A

This is to certify that, on the basis of the tests undertaken as per Report No.: **HTT202009359CH**, the submitted sample of the above item complies with:

IEC 62321-4:2013+A1:2017
 IEC 62321-5:2013
 IEC 62321-6:2015
 IEC 62321-7-1:2015
 IEC 62321-7-2:2017
 IEC 62321-8:2017

And fulfills testing requirement of the RoHS directive 2011/65/EU Annex II amending Annex (EU)2015/863 and amending Annex (EU)2017/2102

RoHS

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19.Question:

故障	排除
不显示 (HDMI 输出)	1、4K 60Hz 显示屏，确认 HDMI 线材是否为 HDMI2.0。 2、将显示器分辨率降为 2K 60Hz。
设备管理员出现惊叹号	1、原系统显卡驱动删除干净。(请使用 Display Driver Uninstaller 删除， https://www.wagnardsoft.com/) 2、Windows 10 版本 16299 不支持，需更新至最新版 (17763、17134 均可)。 3、Windows 7 打补丁 KB2685811。 4、确认核显驱动是否已安装。(不需独立显卡显示) 5、安装在移动平台上，主板 BIOS 需关闭电池管理。
显卡不工作	量下 3V3/5V/2V/Reset 信号是否有电/短路？若有电，显卡就会工作。
显卡无法辨视	检查 PCIE LANE Numbering Reversal 设置。
安装 Linux 系统，图形界面异常/无限循环登陆界面，无法登录系统	装驱动的时，加上--no-opengl-files。 (禁用 opengl，若系统上有用到这套组件，将无法使用)
控制面板无法正常开启	仅可在独显显示时，才能正常开启；若使用集显显示，将无法正常开启。
集显及独显在 Win7 下，无法同时开启 3D 启动	打系统补丁 (KB2685811)，解压后，把所有文件 COPY 到 C 盘 根目录下运行 BAT 文件。
X86 在中标麒麟系统下，显卡驱动后集显输出不能进系统	将 xorg.conf 文件内容清空。

20.Packing:

Item	N.W(KG)	G.W(KG)	Dimension(mm)	Q'ty(PCS)
Bulk Packing	2.1	4	505*340*198	60

21.Part Number:

Model	Part Number	Specification
MD1030A5-2G(PH)	8.ZRT.80-6304-01-LFF	F63 GT1030 2G 64bit GDDR5 256*32 DP+HDMI MXM Type A 6L-工包六十入
	8.ZRT.80-6304-06-LFF	F63 GT1030 2G 64bit GDDR5 256*32 DP+HDMI MXM Type A 6L-ZRT 彩包二十入
MD1030A5-2G(HD)	8.ZRT.80-6304-00-LFF	F63 GT1030 2G 64bit GDDR5 256*32 HDMI+DVI MXM Type A 6L-工包
	8.ZRT.80-6304-05-LFF	F63 GT1030 2G 64bit GDDR5 256*32 HDMI+DVI MXM Type A 6L-ZRT 彩包二十入
MD1030A5-2G(HD/-30)	8.ZRT.80-6304-07-LFF	F63 GT1030 2G 64bit GDDR5 256*32 HDMI+DVI MXM Type A 6L -30 度-ZRT 彩包二十入
MD1030A5-2G(15W)	8.ZRT.80-6304-02-LFF	F63 GT1030 2G 64bit GDDR5 256*32 2DP MXM Type A 6L 15W- 工包六十入
	8.ZRT.80-6304-04-LFF	F63 GT1030 2G 64bit GDDR5 256*32 2DP MXM Type A 6L 15W- ZRT 彩包二十入